

**MULTI-USER INTERFERENCE RESILIENT
ULTRA WIDEBAND (UWB) COMMUNICATION**

[0001] This application claims priority from U.S. Provisional Application Serial No.60/453,809, filed March 8, 2003, the entire content of which is incorporated herein by reference.

**STATEMENT REGARDING FEDERALLY SPONSORED RESEARCH OR
DEVELOPMENT**

[0002] This invention was made with Government support under Subcontract #497420 awarded by the University of Delaware (Army Prime #DAAD19-01-2-011). The Government may have certain rights in the invention.

TECHNICAL FIELD

[0003] The invention relates to communication systems and, more particularly, transmitters and receivers for use in multi-user ultra wideband (UWB) communication systems.

BACKGROUND

[0004] In multi-user wireless communication systems, such as wireless local area networks, satellite communications and mobile phone networks, multiple transmitters and receivers may communicate simultaneously through a common wireless communication medium. One communication format with attractive features for high data rates and low power consumption is ultra wideband (UWB), also known as impulse radio (IR), in which the transmitter generates a train of ultra short pulses spreading the energy of the transmitted signal across an ultra wide bandwidth. IR technology employing time-hopping (TH) codes pseudo randomly spreads pulses in time, and is often referred to IR multiple access (IRMA). [0005] In general, IRMA applies a TH code chosen from a set of orthogonal "spreading codes" to an outbound stream of "symbols." Each IRMA pulse transmits a "symbol" representing a discrete information bearing value selected from a finite set ("alphabet"). For

example, simple alphabets used by transmitters may be $\{+1, -1\}$ or $\{-3, -1, +1, +3\}$. TH codes apply a finite set of integer values to time shift a sequence of transmitted pulses and produce a set of “chips” for each value to be transmitted. Each TH code has a sequence, resulting in each IRMA pulse train having a defined time period. The resulting chips are transmitted according to some modulation scheme, such as pulse position modulation (PPM). In order to separate signals from multiple users, the receivers isolate the signal of the desired user by matching the user’s signal to the corresponding orthogonal spreading code associated with that user.

[0006] IRMA systems often operate in dense multi-path environments. IR signaling provides resolvable multi-path components and enables collecting energy from multi-path propagation with appropriate receiver design. However, when multiple users are present, the multi-path propagation also induces multi-user interference (MUI). In addition, the communication channel between the transmitter and the receiver can also become “frequency selective” in that certain frequencies exhibit fading, i.e. significant loss of signal. Consequently, inter-symbol interference (ISI) in which the transmitted symbols interfere with each other destroys the orthogonality of the waveforms at the receiver. MUI, together with ISI, can cause the receiver to be unable to correctly separate the multi-user waveforms, eventually leading to data loss and/or bandwidth and power inefficiencies.

[0007] Various “multi-user detectors” have been developed for separating non-orthogonal UWB multi-user waveforms. These multi-user detectors, however, are often complex and expensive to implement in typical mobile communication devices, and typically require knowledge of the characteristics of the current communication channel. For example, linear detectors, such as decorrelating and minimum mean-squared error (MMSE) detectors, often require inversion of large matrices with size increasing in proportion to the square of the number of users, while optimum maximum likelihood (ML) detectors entail exponential complexity. In addition, some analog IRMA systems approximate MUI as Gaussian noise and attempt to suppress it statistically. Such systems require successful application of strict power control and rely on the Gaussian approximation. However, when the number of users is not sufficiently large, the Gaussian approximation is not valid.

SUMMARY

[0008] In general, techniques are described for deterministically substantially eliminating multi-user interference (MUI) in multi-user wireless communication systems, such as ultra wideband (UWB) systems. Unlike conventional IRMA systems in which MUI is approximated as Gaussian noise and rely on time-hopping (TH) spreading codes to statistically suppress MUI, “multi-stage block-spreading” (MS-BS) techniques are described that eliminate MUI deterministically at the receiver by preserving the orthogonality between different users waveforms through multi-path channels. Furthermore, the techniques described herein allow a larger number of users than conventional IRMA systems and can provide different users with variable transmission rates.

[0009] In one embodiment, the invention is directed to a method which generates a stream of frames from blocks of information bearing symbols wherein the frames corresponding to different blocks of the symbols are interleaved and generates a stream of chips from the stream of frames, wherein the chips corresponding to different frames are interleaved. An ultra wideband (UWB) transmission signal is output from the stream of chips. The stream of frames is generated by parsing the symbols into blocks of K symbols, applying an orthogonal set of spreading codes to the blocks of K symbols to form Q frames, and interleaving the Q frames to form the stream of frames. The stream of chips is generated by applying an orthogonal set of time-hopping spreading codes to the interleaved frames to generate a plurality of chips for each frame, and interleaving each of the plurality of chips to form the output stream of chips.

[0010] The method may further assign each of the set of spreading codes to a different user of a group of users and assign each user of the group a common one of the time-hopping spreading codes. Unique addresses are assigned to users as unique pair-wise combinations of the set of orthogonal spreading codes and the set of time-hopping spreading codes.

[0011] In additional embodiments, the method receives the output signal and outputs a stream of estimate symbols from the signal using a two-stage de-spreading unit having a time-hopping de-spreading module and a multi-user de-spreading module.

[0012] In another embodiment, the invention is directed to a wireless communication device having a multiple-user block-spreading unit that generates a set of frames for respective blocks of information bearing symbols and produces a stream of frames in which the frames

from different sets are interleaved, a time-hopping block-spreading unit that generates a set of chips for each frame and outputs a stream of chips in which the chips generated from different frames are interleaved, and a pulse shaping unit to output an UWB transmission signal from the stream of interleaved chips.

[0013] In yet another embodiment, the invention is directed to a wireless communication device having a two-stage de-spreading unit that processes a received ultra wideband (UWB) transmission signal to produce estimate symbols, wherein the received UWB signal comprises a multi-user block-spread UWB signal formed from interleaved symbol frames and interleaved chips within the symbol frames.

[0014] In additional embodiments, the invention is directed to a system having a wireless transmitter to transmit an ultra wideband (UWB) signal according to interleaved chips generated from interleaved frames produced by blocks of information bearing symbols and wireless receiver to receive the UWB signal and de-interleave the chips and frames to produce estimate symbols.

[0015] In another embodiment, the invention is directed to a computer-readable medium containing instructions. The instructions cause a programmable processor of a wireless communication device to generate a stream of frames from blocks of information bearing symbols, wherein the frames corresponding to different blocks of the symbols are interleaved, generate a stream of chips from the stream of frames, wherein the chips corresponding to different frames are interleaved, and output an UWB transmission signal from the stream of chips.

[0016] The multi-stage block-spreading techniques described herein may offer one or more advantages. For example, MS-BS preserves the orthogonality of a transmitted UWB signal through a multi-user communication channel. Consequently, transmitted signals are resistant to MUI regardless of the underlying frequency selective nature of the multi-path communication channel without using adaptive power control to dynamically adjust the usage of power by transmitter. As a result, the techniques render the multiple access communication channel equivalent to a set of independent parallel single-user frequency-selective channels with additive white Gaussian noise (AWGN). In other words, because the transmitted signals remain orthogonal, IRMA receivers may be implemented with single-user

detectors, which are less complex than multi-user detectors. Furthermore, the receiver can be implemented in favor of low complexity or high performance.

[0017] Other advantages of the described techniques may include improved bandwidth efficiency, which implies that information can be transmitted at higher rates and that the maximum allowable number of simultaneous users increases. Furthermore, as the number of users increases, an IRMA system that incorporates the described techniques may be more resilient to degradation in bit-error-rate (BER) performance than conventional IRMA systems.

[0018] The details of one or more embodiments of the invention are set forth in the accompanying drawings and the description below. Other features, objects, and advantages of the invention will be apparent from the description and drawings, and from the claims.

BRIEF DESCRIPTION OF DRAWINGS

[0019] FIG. 1 is a timing diagram illustrating an example time-hopping (TH) pulse position modulation (PPM) scheme in an impulse radio multiple access (IRMA) system.

[0020] FIG. 2 is a block diagram illustrating a wireless system impulse radio multiple access (IRMA) format in which multiple transmitters communicate with multiple receivers through a channel.

[0021] FIG. 3 is a block diagram illustrating in further detail the multi-user UWB communication system of FIG. 2.

[0022] FIG. 4 illustrates an example assignment of multiple users (MU) and time-hopping (TH) addresses.

[0023] FIG. 5 is a block diagram illustrating an example transmission of a symbol using PPM in an IRMA system.

[0024] FIG. 6 illustrates an example data stream generated by a multi-user block-spreading unit within a UWB transmitter of FIG. 3.

[0025] FIG. 7 illustrates an example data stream generated by a TH block-spreading unit within the transmitter of FIG. 3.

[0026] FIG. 8 is a flowchart illustrating an example mode of operation of a transmitter in the UWB communication system of FIG. 3.

[0027] FIG. 9 is a flowchart illustrating an example mode of operation of a receiver in the UWB communication system of FIG. 3.

[0028] FIGS. 10-13 are graphs illustrating modeled performance estimates of the techniques described herein.

DETAILED DESCRIPTION

[0029] The techniques described herein use notation defined as follows. Bold upper case letters and lower case letters denote matrices and column vectors respectively. $()^T$ and $()^H$ denote transpose and Hermitian transpose respectively. $\delta()$ and \otimes stand for Kroenecker's delta and Kronecker's product respectively. $E\{\}$ denotes expectation. $\lfloor \cdot \rfloor$ and $\lceil \cdot \rceil$ denote integer floor and integer ceiling respectively. \mathbf{I}_K denotes the identity matrix of size K and $\mathbf{0}_{M \times N}$ denotes an all zero matrix of size $M \times N$.

[0030] FIG. 1 is a timing diagram that illustrates an example transmission of two separate symbols, 1 (1A) and 0 (1B), using binary pulse position modulation (PPM) in an impulse radio multiple access (IRMA) format. Generally, the PPM-IRMA format described herein is designed such that the PPM delays are sufficiently large to ensure orthogonal modulation using M -ary PPM. As a result, a nonlinear PPM-IRMA format can be viewed as a linearly modulated code-division multiple access (CDMA) format.

[0031] Each symbol 1A, 1B represents a discrete information bearing value selected from a finite set ("alphabet"), e.g. $\{1, 0\}$. Symbols 1A, 1B are repeated over N_f frames, e.g. $N_f = 5$, with each frame 2 having duration T_f 4. During signaling interval T_s 6 of duration $T_s = N_f T_f$ seconds, $k_b = \log_2 M$ message bits having bit rate R_b may be loaded into a k_b -bit buffer. The resulting k_b -bit output symbol rate is $R_s = R_b/k_b$. For example, the u^{th} user's information bearing symbol transmitted during the k^{th} frame is denoted $I_u(\lfloor k/N_f \rfloor)$ where $I_u(\lfloor k/N_f \rfloor) \in [M-1]$.

[0032] The application of a time-hopping (TH) spreading code to the information bearing symbols produces a set of "chips" over which each symbol 1A, 1B is transmitted. Each frame 12 comprises N_c , e.g. $N_c = 3$, chips with each chip 8 having chip duration T_c 9. Frame duration T_f is then equivalent to $N_c T_c + T_g$, where T_g is a guard time to account for processing delay at the receiver between two successively received frames. For simplicity T_g is set to zero for all following equations. A nonzero value for T_g does not impose any

limiting consequences. The u^{th} user's transmitted waveform $v_u(t)$ 18 is given in accordance with equation (1):

$$v_u(t) = P_u \sum_{k=-\infty}^{+\infty} w(t - kT_f - c(k)T_c - \tau_{I_u(\lfloor k/N_f \rfloor)}) \quad (1)$$

where P_u is the u^{th} user's transmission power, $w(t)$ denotes the ultra-short pulse 7 and $c_u(k) \in [0, N_c - 1]$ 11 is a periodic pseudo random sequence with period P_c equal to N_f . Ultra-short pulse 7 typically has a duration between 0.20 and 2.0 nanoseconds and may be selected as a Gaussian monocycle, a Gaussian biphasic monocycle, a doublet consisting of a positive Gaussian pulse followed by its negative, and the like. The role of $c_u(k)$ 3 is to enable both multiple users over a communication channel and security.

[0033] Each signaling interval T_s 6 of transmitted waveform 18 in equation (1) includes N_f copies of a single symbol 1A, 1B, i.e. one per frame 2, with pulse 7 time-shifted in each frame 2 according to the symbol value, e.g. it is shifted by τ_m for $I_u(\lfloor k/N_f \rfloor) = m$ where $m \in [0, 1, \dots, M-1]$. In order to ensure orthogonal modulation, PPM modulation delays τ_m must satisfy $\tau_m - \tau_{m-1} \geq T_w$ for $\forall m \in [1, 2, \dots, M-1]$. Thus, chip duration T_c 9 should be chosen to satisfy $T_c \geq \tau_{m-1} + T_w \geq MT_w$.

[0034] The orthogonal M-ary PPM of the u^{th} user can be viewed as having M parallel branches with each parallel branch realizing a shifted version of the pulse stream. Consequently, in order to generate the waveform $v_u(t)$ as given in equation (1), only one branch out of the M parallel branches needs to be selected depending on the symbol value. Summing the M parallel branches, waveform $v_u(t)$ can be rewritten in terms of waveform $v_{u,m}(t)$ as given in equations (2) and (3) respectively.

$$v_u(t) = \sum_{m=0}^{M-1} v_{u,m}(t) \quad (2)$$

$$v_{u,m}(t) = P_u \sum_{k=-\infty}^{k=+\infty} s_{u,m}(\lfloor k/N_f \rfloor) w(t - kT_f - c(k)T_c - \tau_m) \quad (3)$$

The waveform $s_{u,m}(\lfloor k/N_f \rfloor) := \delta(I_u(\lfloor k/N_f \rfloor) - m)$, $\forall m \in [0, M-1]$ (12). Equation (3) can be rewritten as equation (4) by defining monocycles 7 as pulse functions $w_m(t) := w(t - \tau_m)$ and using the substitution $T_f = N_c T_c$.

$$v_{u,m}(t) = P_u \sum_{k=-\infty}^{k=+\infty} s_{u,m}(\lfloor k/N_f \rfloor) w_m(t - (kN_c + \hat{c}(k))T_c) \quad (4)$$

[0035] Because $\hat{c}(k) \in [0, N_c-1]$ is an integer, $w_m(t)$ is shifted by an integer multiple of T_c . As a result waveform $v_{u,m}(t)$ can be written in equation (5) accordingly. Equation (5) can be viewed as a linearly modulated waveform with symbol rate $R_s = 1/T_c$ while equation (2) can be viewed as the superposition of M linear modulators, each with a different pulse function $w_m(t)$.

$$v_{u,m}(t) = P_u \sum_{k=-\infty}^{k=+\infty} v_{u,m}(n) w_m(t - nT_c) \quad (5)$$

Sequence $v_{u,m}(n)$ is dependent on $s_{u,m}(k)$ and $\hat{c}(k)$. Chip-rate code sequence $c_u(n)$ with period $P_c = N_c$ is defined via $\hat{c}(k)$ as given in equation (6).

$$c_u(n) := \delta(\lfloor n/N_c \rfloor N_c + \hat{c}_u(\lfloor n/N_c \rfloor) - n) \quad (6)$$

The relation between $\hat{c}_u(k)$ and $c_u(n)$ is given by the mapping between chip index n and frame index k . As a result, the u^{th} user's chip sequence on the m^{th} branch $v_{u,m}(n)$ can be expressed according to equation (7).

$$v_{u,m}(n) = s_{u,m}(\lfloor n/N_c N_f \rfloor) c_u(n) \quad (7)$$

[0036] Sequence $s_{u,m}(\lfloor n / (N_c N_f) \rfloor)$ does not change over the duration of $N_f N_c$ chips ($N_f T_f$ seconds) and is spread by $c_u(n)$ in equation (2) to generate chip sequence $v_{u,m}(n)$ in equation (7). From this chip-rate sampled model, the nonlinearly modulated PPM-IRMA format can be viewed as a linearly modulated code-division multiple access (CDMA) format.

[0037] Although T_g was set equal, the model can also include T_g as nonzero. This can be accomplished by setting $T_g = N_g T_c$ with N_g being an integer and restricting $c(k)$ to take on values $[0, N_c' - 1]$, where $N_c' = N_c - N_g$. Hereafter, all equations and derivations will assume $T_g = 0$.

[0038] FIG. 2 is a block diagram illustrating multi-user wireless communication system 10 in which multiple transmitters 12 communicate with receivers 14 through wireless channel 16 via the M-ary PPM-IRMA format described in FIG. 1. In general, the invention provides techniques for deterministically substantially eliminating multi-user interference (MUI) 18 in communication system 10 that could be introduced in waveforms produced by transmitters 12 during transmission through communication channel 16.

[0039] Transmitters 12 transmit information using techniques referred to herein as “multi-stage block-spreading” (MS-BS). MS-BS generates a stream of frames from blocks of information symbols wherein the frames corresponding to different blocks of the symbols are interleaved and a stream of chips is generated from the stream of frames wherein the chips corresponding to different frames are interleaved. Transmitters 12 output a UWB transmission signal from the stream of chips.

[0040] The stream of frames is generated by applying an orthogonal set of spreading codes, such as direct sequence CDMA codes, TDMA codes, or FDMA codes, while the stream of chips is generated by applying an orthogonal set of TH spreading codes. The stream of chips may be padded with a number of guard chips determined as a function of length of channel 16. The set of spreading codes and the set of TH spreading codes are mutually exclusive so that the interleaved and padded chips retain their orthogonality after passing through channel 16. Each user of a group of users is assigned a spreading code from the set of spreading codes and is also assigned a common TH spreading code from the set of TH spreading codes. System 10 supports a total number of users determined by the product of the number of

frames over which an information symbol is repeated N_f and the number of chips in each frame N_c .

[0041] MS-BS techniques preserve the orthogonality between different users signals through communication channel 16 regardless of the underlying frequency selective nature of the multi-path communication channel thereby rendering channel 16 equivalent to a set of independent parallel single-user frequency-selective channels with additive white Gaussian noise (AWGN).

[0042] Receivers 14 receive the UWB transmission signal output by transmitters 12 and output a stream of estimate symbols from the received signal using a two-stage de-spreading unit having a TH de-spreading module and a multi-user de-spreading module. The stream of output symbols is generated by converting the received signal to a stream of chips and applying a first de-spreading matrix with the TH de-spreading module to de-interleave the chips into blocks of frames. The multi-user de-spreading module applies a second de-spreading matrix to the blocks of frames to de-interleave the frames and produce blocks of estimate symbols. A single user detection scheme is applied to the blocks of estimates symbols to output the stream of the estimate symbols.

[0043] These techniques work with existing communication systems using IRMA with orthogonal M-ary pulse position modulation (PPM) to eliminate MUI 18 deterministically at receivers 14 and apply to uplink and downlink transmissions, i.e., transmissions from a base station to a mobile device and vice versa. Transmitters 12 and receivers 14 may be any device configured to communicate using a multi-user wireless transmission including a hub for a wireless local area network, a cellular phone, a laptop, handheld computing device, a personal digital assistant (PDA), or a cellular distribution station, and the like.

[0044] The techniques described herein may be applied to uplink and/or downlink transmissions, i.e., transmissions from a base station to a mobile device and vice versa. Consequently, transmitter 12 and receiver 14 may be any device configured to communicate using a multi-user ultra wideband wireless transmission including a distribution station, a hub for a wireless local area network, a mobile phone, a laptop or handheld computing device, a personal digital assistant (PDA), a device within a wireless personal area network, a device within a sensor network, or other device.

[0045] FIG. 3 is a block diagram illustrating in further detail multi-user communication system 10 of FIG. 2 using multi-stage block-spreading (MS-BS). For simplicity, the m^{th} branch of a transmitter 12 and the m^{th} branch at a receiver 14 are given in detail for the u^{th} user. Transmitter 12 applies MS-BS techniques and pulse shaping to chip-rate information bearing symbols $s_u(m,n)$ 12 and transmits the MS-BS M-ary pulse position modulated (PPM) IRMA waveform $v_{u,m}(t)$ 28. Receiver 14 receives chip-rate sampled sequence $x_m(n)$ 38 and outputs symbol estimates 49.

[0046] Generally, multiple transmitters 12 corresponding to different users assign each user a unique spreading code from a set of orthogonal spreading codes, such as direct sequence CDMA codes TDMA codes, or FDMA codes. Additionally each user is assigned a TH spreading code from a set of orthogonal spreading codes such that within a group of users, each user with a unique orthogonal spreading code is also assigned a common TH spreading code. Therefore, a unique address $\{u_A, u_B\}$ can be assigned to each user with the first index of the address indicating the TH code assigned to the u^{th} user and the second index indicating the orthogonal spreading code assigned to the u^{th} user.

[0047] Serial to parallel (S/P) converter 20 of transmitter 12 parses serial chip-rate data stream $s_{u,m}(n)$ 20 into blocks of K symbols 22, each symbol representing a discrete information bearing value, as defined in equation (8).

$$s_{u,m}(i) := [s_{u,m}(iK), \dots, s_{u,m}(iK + K - 1)]^T \quad (8)$$

[0048] As described in detail below, multi-user block-spreading unit 23 applies an orthogonal spreading code according to the address assigned to the u^{th} that generates a stream of Q frames from blocks of K information bearing symbols and interleaves the frames corresponding to different blocks of the symbols. Each block of K symbols is spread into $N_f K \times 1$, where $N_f K = Q$, output vectors 24 according to equation (9) via $Q \times K$ spreading matrix \mathbf{D}_{uB} given in equation (10).

$$\tilde{s}_{u,m}(i) = \mathbf{D}_{uB} s_{u,m}(i), \text{ where } \tilde{s}_{u,m}(i) := [\tilde{s}_{u,m}(iQ), \dots, \tilde{s}_{u,m}(iQ + Q - 1)]^T \quad (9)$$

$$\mathbf{D}_{uB} := \mathbf{d}_{uB} \otimes \mathbf{I}_K \quad (10)$$

[0049] In other words, the K symbol blocks are symbol-spread into Q frame-rate signals and then frame-interleaved to provide data stream 24 according to equation (9). In rewriting equation (9), it can be seen multi-user block-spreading unit 23 implements a multi-code transmitter with K codes per user as given in equation (11).

$$\check{s}_{u,m}(i) = \sum_{k=0}^{K-1} s_{u,m}(iK + k) \mathbf{d}_{uB}^{(k)} \quad (11)$$

Each column of spreading matrix \mathbf{D}_{uB} can be viewed as a separate spreading code for the u^{th} user, with the $(k + 1)^{\text{st}}$ column denoted by $\mathbf{d}_{uB}^{(k)}$.

[0050] Similarly, TH spreading unit 25 applies a TH spreading code selected from a set of mutually orthogonal TH spreading codes according to the address assigned to the u^{th} user. Each block of Q frame-rate signals of data stream 43 is spread into a stream of P chips wherein the chips corresponding to different frames are interleaved. In other words, TH block-spreading unit spreads a block of Q frame-rate signals into P chip-rate signals followed by chip interleaving and zero padding via spreading matrix \mathbf{C}_{uA} according to equation (12).

$$\mathbf{v}_{u,m}(i) = \mathbf{C}_{uA} \check{s}_{u,m}(i) \quad (12)$$

TH spreading matrix \mathbf{C}_{uA} is derived from equations (13, 14) and defined in equation (15).

$$\mathbf{c}_{uA}^{(q)} := [\mathbf{c}_{uA}(qN_c), \mathbf{c}_{uA}(qN_c + 1), \dots, \mathbf{c}_{uA}(qN_c + N_c - 1)]^T, \text{ for } q \in [0, N_f - 1] \quad (13)$$

Recall that \mathbf{c}_{uA} is given by equation (6). Matrix $\mathbf{C}_{uA}^{(q)}$ is defined in equation (14).

$$\mathbf{C}_{uA}^{(q)} := \mathbf{c}_{uA}^{(q)} \otimes \mathbf{T}_{zp}, \text{ where } \mathbf{T}_{zp} := [\mathbf{I}_K, \mathbf{0}_{K \times L}]^T \text{ for } (14)$$

Zero-padding matrix \mathbf{T}_{zp} is a $(K + L) \times K$ matrix and appends L zeros at the end of each column upon multiplication. The guard chips may be null values as implemented here with zero padding via matrix multiplication. TH spreading matrix \mathbf{C}_{uA} can then be written as in equation (16).

$$\mathbf{C}_{uA} := \text{diag}\{ \mathbf{C}_{uA}^{(0)}, \mathbf{C}_{uA}^{(1)}, \dots, \mathbf{C}_{uA}^{(N_f-1)} \} \quad (15)$$

TH spreading matrix \mathbf{C}_{uA} is of size $P \times Q$, where $P = N_f N_c (K + L)$. The parameter L is determined by the effective length of channel 16 in discrete time and is calculated below. As a result, data stream 26 at the output of TH block-spreading unit 25 is given by the $P \times 1$ vector on the m^{th} branch in equation (16) and is given in matrix-vector form according to equation (17).

$$\mathbf{v}_{u,m}(i) := [v_{u,m}(iP), \dots, v_{u,m}(iP + P - 1)]^T \quad (16)$$

$$\mathbf{v}_{u,m}(i) = \mathbf{C}_{uA} \mathbf{D}_{uB} \mathbf{s}_{u,m}(i) \quad (17)$$

[0051] At the output of the two-stage MS_BS parallel to serial (P/S) converter 27 parses blocks of P chips into serial data stream $v_{u,m}(n)$ 28. MS-BS techniques may also offer different users variable transmission rates. Assigning the u^{th} user a set of A_u MU and TH address pairs denoted by Ω_u allows equation (17) to be rewritten as equation (18).

$$\mathbf{v}_{u,m}(i) = \sum_{\{\omega_A, \omega_B\} \in \Omega_u} \mathbf{C}_{\omega A} \mathbf{D}_{\omega B} \mathbf{s}_{u,m}^{\{\omega_A, \omega_B\}}(i) \quad (18)$$

In other words, the u^{th} user can transmit A_u symbol blocks of length K simultaneously, each carried on a distinct address pair $\{\omega_A, \omega_B\} \in \Omega_u$. Consequently, the u^{th} user can transmit a total number of $K_u = A_u K$ symbols during each burst of PT_c seconds with a corresponding transmission rate $R_u = K_u / (PT_c)$. In order to avoid address collisions and guarantee deterministic symbol block separation, the address must be assigned such that additional conditions (19, 20) are satisfied.

$$\bigcup_u \Omega_u \in \Omega, \forall u \in [0, N_u - 1] \quad (19)$$

$$\Omega_u \cap \Omega_\mu = \emptyset, \forall u, \mu \in [0, N_u - 1], \text{ and } u \neq \mu \quad (20)$$

[0052] Data stream 28 propagates through equivalent channel 30 which includes pulse shaper 32, physical channel 16, AWGN noise 34, and matched filter 36. Pulse shaper 32 converts serial data stream 28 to the analog transmitted signal 31 $v_{u,m}(t)$ by varying the interval between pulses according to the data being modulated and according to the assigned TH spreading code by applying previously defined pulse shaping functions, $w_m(t) := w(t - \tau_m)$. Analog signal 31 propagates through multi-path frequency selective physical channel 16, denoted $g_u(t)$ in which AWGN 34 is added to the signal and is filtered by matched filter 36 on the m' th branch, $\bar{w}_{m'}(t)$, matched to $\bar{w}_{m'}(t)$ where $m' \in [0, M-1]$.

[0053] The chip-sampled discrete time equivalent finite impulse response (FIR) channel can be represented according to equation (21) where $*$ denotes convolution.

$$h_{u,m'}(l) := (w_m * g_u * \bar{w}_{m'})(t) \big|_{t=lT_c} \quad (21)$$

Equivalent FIR channel of equation (21) of order L_u includes the u^{th} user's asynchronism in the form of delay factors as well as transmit-receive filters, and the multi-path effects.

AWGN 34, denoted $\eta(t)$, is effectively sampled at chip-rate, $t = nT_c$, and can be represented as sampled AWGN noise according to equation (22).

$$\eta_{m'}(n) := (\eta * \bar{w}_{m'})(t) \big|_{t=nT_c} \quad (22)$$

Receiver 14 receives the chip-sampled matched filter output 28 from matched filter 36 as given in equation (23).

$$x_{m'}(n) = \sum_{u=0}^{N_u-1} \sum_{m=0}^{M-1} \sum_{l=0}^L P_u h_{u,m,m'}(l) v_{u,m}(n-l) + \eta_{m'}(n) \quad (23)$$

N_u is the number of users, L is the maximum length of communication channel 16 in discrete time for the u^{th} user, and M is the number of PPM pulse shapers.

[0054] Asynchronism among users in the uplink of a quasi-synchronous system, i.e. a system in which there is a coarse timing reference, is limited to a few chip intervals. The maximum asynchronism, $\tau_{\max,a}$, arises between the nearest and the farthest mobile users and the maximum multi-path spread, $\tau_{\max,s}$, can be found using field measurements from the operational environment. The maximum channel order can then be determined by equation (24).

$$L = \lceil (\tau_{\max,s} + \tau_{\max,a})/T_c \rceil \quad (24)$$

The downlink model, e.g. from the base station to user of interest μ , is subsumed by the uplink model presented above by setting $h_{u,m,l}(l) = h_{u,m,l}(l)$, $\forall u \in [0, N_u-1]$, since the latter allows for distinct user channels. Downlink transmissions are synchronous with $\tau_{\max,a} = 0$ and maximum channel order L depends only on $\tau_{\max,s}$ through $L = \lceil (\tau_{\max,s}/T_c) \rceil$. The only channel knowledge assumed at transmitters 12 in the uplink or downlink model is L .

[0055] At receiver 14, serial to parallel unit 40 converts chip-rate sampled sequence 38 into $P \times 1$ blocks 41 as given in equation (25).

$$\mathbf{x}_m(i) := [x_m(iP), x_m(iP+1), \dots, x_m(iP+P-1)]^T \quad (25)$$

Chip-rate sampled blocks 41 are received in the presence of $P \times 1$ noise blocks given in equation (26).

$$\boldsymbol{\eta}_m(i) := [\eta_m(iP), \eta_m(iP+1), \dots, \eta_m(iP+P-1)]^T \quad (26)$$

Allowing $\mathbf{H}_{u,m,m,0}$ to be the $P \times P$ lower triangular Toeplitz matrix with first column $[h_{u,m,m}(0), \dots, h_{u,m,m}(L), 0, \dots, 0]^T$ and $\mathbf{H}_{u,m,m,1}$ be the $P \times P$ upper triangular Toeplitz matrix with first row $[0, \dots, 0, h_{u,m,m}(L), \dots, h_{u,m,m}(1)]^T$ the input-output block relationship through equivalent channel 30 can be described in matrix form according to equation (27).

$$\mathbf{x}_m(i) = \sum_{u=0}^{N_u-1} \sum_{m=0}^{M-1} P_u [\mathbf{H}_{u,m',m,0} \mathbf{v}_{u,m}(i) + \mathbf{H}_{u,m',m,1} \mathbf{v}_{u,m}(i-1) + \boldsymbol{\eta}_m(i)] \quad (27)$$

With $\mathbf{v}_{u,m}(i)$ as defined in equation (17) and making use of the mathematical address assigning rules defined below, equation (27) can be rewritten as given in equation (28).

$$\mathbf{x}_m(i) = \sum_{u_A=0}^{N_e-1} \sum_{u_B=0}^{N_f-1} \sum_{m=0}^{M-1} P_u [\mathbf{H}_{u,m',m,0} \mathbf{C}_{uA} \mathbf{D}_{uB} \mathbf{s}_{u,m}(i) + \mathbf{H}_{u,m',m,1} \mathbf{C}_{uA} \mathbf{D}_{uB} \mathbf{s}_{u,m}(i-1) + \boldsymbol{\eta}_m(i)] \quad (28)$$

The $\mathbf{s}_{u,m}(i-1)$ dependent term in equation (28) accounts for inter-block interference (IBI) and will be shown to equal a $\mathbf{0}_{P \times 1}$ matrix.

[0056] Receiver 14 performs multi-user separation on the $P \times 1$ chip rate sampled blocks 41 by applying de-spreading matrices $\check{\mathbf{C}}_{uA}$ and $\check{\mathbf{D}}_{uB}$ in TH de-spreading unit 42 and multi-user de-spreading unit 44 respectively. $P \times N_f(K+L)$ TH de-spreading matrix $\check{\mathbf{C}}_{uA}$ and $N_f(K+L) \times (K+L)$ multi-user de-spreading matrix $\check{\mathbf{D}}_{uB}$ are given in equations (29, 30) respectively.

$$\check{\mathbf{C}}_{uA} := \text{diag} \{ \check{\mathbf{C}}_{uA}^{(0)}, \check{\mathbf{C}}_{uA}^{(1)}, \dots, \check{\mathbf{C}}_{uA}^{(N_f-1)} \} \quad (29)$$

$$\check{\mathbf{D}}_{uB} := \mathbf{d}_{uB} \otimes \mathbf{I}_{K+L} \quad (30)$$

TH de-spreading matrix $\check{\mathbf{C}}_{uA}$ is similarly derived as \mathbf{C}_{uA} from equations (6, 13) and equation (31).

$$\check{\mathbf{C}}_{uA}^{(q)} := \mathbf{c}_{uA}^{(q)} \otimes \mathbf{I}_{K+L} \quad (31)$$

TH de-spreading unit 42 and MU de-spreading unit 44 perform multi-stage block-de-spreading and enable separation of the μ^{th} user's signal from superimposed multi-user signals deterministically. TH de-spreading matrix $\check{\mathbf{C}}_{uA}$ can be viewed as chip-de-interleaving followed by block-de-spreading. Similarly, multi-user de-spreading matrix $\check{\mathbf{D}}_{uB}$ can be viewed as frame-de-interlaving followed by block-de-spreading.

[0057] Transceiver pairs $\{ \mathbf{C}_{uA}, \check{\mathbf{C}}_{uA} \}_{uA=0}^{N_c-1}$ and $\{ \mathbf{D}_{uB}, \check{\mathbf{D}}_{uB} \}_{uB=0}^{N_f-1}$ are designed such that superimposed multi-user signals can be separated deterministically regardless of multi-path propagation through frequency selective ISI channels of order L . The properties of the Kronecker product can be used to verify the following mutual orthogonality relationships between any two users u and μ as given in equations (32-35).

$$\mathbf{C}_{\mu A}^H \mathbf{C}_{uA} = \delta(\mu_A - u_A) \mathbf{I}_{N_f K} \quad (32)$$

$$\check{\mathbf{C}}_{uA}^H \check{\mathbf{C}}_{\mu A} = \delta(\mu_A - u_A) \mathbf{I}_{N_f(K+L)} \quad (33)$$

$$\mathbf{D}_{\mu B}^H \mathbf{D}_{uB} = N_f \delta(\mu_B - u_B) \mathbf{I}_K \quad (34)$$

$$\check{\mathbf{D}}_{\mu B}^H \check{\mathbf{D}}_{uB} = N_f \delta(\mu_B - u_B) \mathbf{I}_{K+L} \quad (35)$$

[0058] The multiple-stage block-de-spreading performed by TH de-spreading unit 42 and multi-user de-spreading unit 44 separate the μ^{th} user's signal from 41 and output MUI free block according to equation (36)

$$\mathbf{y}_{\mu, m'}(i) = \check{\mathbf{D}}_{\mu B}^H \check{\mathbf{C}}_{\mu A}^H \mathbf{x}_{m'}(i), \forall m' \in [0, M-1] \quad (36)$$

[0059] MUI free output 45 given by equation (37) can then be input into any single-user detector 46 to eliminate channel effects and output symbol block estimates.

$$\mathbf{y}_{\mu}(i) := [\mathbf{y}_{\mu, 0}^T(i), \mathbf{y}_{\mu, 1}^T(i), \dots, \mathbf{y}_{\mu, M-1}^T(i)]^T \quad (37)$$

Equations (36, 37) can be rewritten to explicitly show that the superimposed received signals from multiple users can be separated deterministically regardless of the FIR multi-path channel when choosing transceiver pairs $\{ \mathbf{C}_{uA}, \check{\mathbf{C}}_{uA} \}_{uA=0}^{N_c-1}$ and $\{ \mathbf{D}_{uB}, \check{\mathbf{D}}_{uB} \}_{uB=0}^{N_f-1}$. By design, the last L rows of TH spreading matrix \mathbf{C}_{uA} are zeros while the nonzero elements of $\mathbf{H}_{u, m', m, l}$ appear only in its last L columns. Equations (38, 39) prove IBI interference is deterministically eliminated.

$$\mathbf{H}_{u,m',m,l} \mathbf{C}_{uA} = \mathbf{0}_{PxN_fK} \quad (38)$$

$$\mathbf{H}_{u,m',m,l} \mathbf{C}_{uA} \mathbf{D}_{uB} \mathbf{s}_{u,m}(i-1) = \mathbf{0}_{Px1} \quad (39)$$

[0060] Further, equations (43, 44) derived from following equations (40-42) explicitly show that the superimposed received signals from multiple users can be separated deterministically after propagation through FIR multi-path channels. This is due to the design of mutually orthogonal transceiver pairs $\{ \mathbf{C}_{uA}, \check{\mathbf{C}}_{uA} \}_{uA=0}^{N_c-1}$ and $\{ \mathbf{D}_{uB}, \check{\mathbf{D}}_{uB} \}_{uB=0}^{N_f-1}$ given in equations (32-35) which preserves the code orthogonality among users. In the following equations $\hat{\mathbf{H}}_{u,m',m}$ is a tall Toeplitz matrix of dimension $(K+L) \times K$.

$$\hat{\mathbf{H}}_{u,m',m} \mathbf{C}_{uA} \mathbf{D}_{uB} \mathbf{s}_{u,m}(i) = \check{\mathbf{C}}_{uA} \check{\mathbf{D}}_{uB} \hat{\mathbf{H}}_{u,m',m} \mathbf{s}_{u,m}(i) \quad (40)$$

$$\mathbf{x}_m(i) = \sum_{uA=0}^{N_c-1} \check{\mathbf{C}}_{uA} \sum_{uB=0}^{N_f-1} \check{\mathbf{D}}_{uB} \sum_{m=0}^{M-1} P_u[\hat{\mathbf{H}}_{u,m',m} \mathbf{s}_{u,m}(i) + \boldsymbol{\eta}_m(i)] \quad (41)$$

Equation (41) is derived by substituting equation (39) into equation (28) and using the equality in equation (40) which shows channel matrix $\mathbf{H}_{u,m',m,0}$ commutes with TH and MU spreading matrices \mathbf{C}_{uA} and \mathbf{D}_{uB} . Using equations (32-35) and equation (41), equation (36) can be rewritten according to equation (42).

$$\mathbf{y}_{\mu,m'}(i) = \check{\mathbf{D}}_{\mu B}^H \check{\mathbf{C}}_{\mu A}^H \sum_{uA=0}^{N_c-1} \sum_{uB=0}^{N_f-1} \check{\mathbf{C}}_{uA} \check{\mathbf{D}}_{uB} \sum_{m=0}^{M-1} P_u[\hat{\mathbf{H}}_{u,m',m} \mathbf{s}_{u,m}(i) + \check{\mathbf{D}}_{\mu B}^H \check{\mathbf{C}}_{\mu A}^H \boldsymbol{\eta}_m(i)] \quad (42)$$

With the substitution $\check{\boldsymbol{\eta}}_m(i) := \check{\mathbf{D}}_{\mu B}^H \check{\mathbf{C}}_{\mu A}^H \boldsymbol{\eta}_m(i)$ equation (42) can be rewritten as equation (43) which shows the specified user, μ , is separated from MUI via multi-stage block-de-spreading $\check{\mathbf{D}}_{\mu B}^H \check{\mathbf{C}}_{\mu A}^H$.

$$\mathbf{y}_{\mu,m'}(i) = N_f P_u \hat{\mathbf{H}}_{\mu,m',m} \mathbf{s}_{\mu,m}(i) + \check{\boldsymbol{\eta}}_m(i) \quad (43)$$

Multi-stage block-de-spreading output can be written in equation (44) using $MK \times 1$ blocks $\mathbf{s}_\mu(i) := [\mathbf{s}_{\mu,0}^T(i), \mathbf{s}_{\mu,1}^T(i), \dots, \mathbf{s}_{\mu,M-1}^T(i)]^T$. Multi-stage block-de-spreading output $\mathbf{y}_{\mu,m}(i)$ and noise $\mathbf{\eta}(i)$ are $M(K+L) \times 1$ vectors generated by concatenating the output blocks from the receiver filters matched to the M waveforms and $\hat{\mathbf{H}}_\mu$ is the $M(K+L) \times MK$ matrix given by equation (45).

$$\mathbf{y}_u(i) = N_f P_u \hat{\mathbf{H}}_\mu \mathbf{s}_\mu(i) + \mathbf{\eta}(i) \quad (44)$$

$$\hat{\mathbf{H}}_\mu := \begin{bmatrix} \check{\mathbf{H}}_{\mu,0,0} & \check{\mathbf{H}}_{\mu,0,1} & \dots & \check{\mathbf{H}}_{\mu,0,M-1} \\ \check{\mathbf{H}}_{\mu,1,0} & \check{\mathbf{H}}_{\mu,1,1} & \dots & \check{\mathbf{H}}_{\mu,1,M-1} \\ \vdots & \vdots & \ddots & \vdots \\ \check{\mathbf{H}}_{\mu,M-1,0} & \check{\mathbf{H}}_{\mu,M-1,1} & \dots & \check{\mathbf{H}}_{\mu,M-1,M-1} \end{bmatrix} \quad (45)$$

MS-BS accomplished by spreading matrix \mathbf{D}_{uB} , which is formed by symbol-spreading followed by frame-interleaving, and spreading matrix \mathbf{C}_{uA} , which is formed by symbol-spreading followed by chip-interleaving and zero padding convert the conventional multi-user detection problem into an equivalent set of single user equalization problems. Similar to multi-path channels with CIBS transmissions, MS-BS cause ISI with each symbol block, but does not cause ICI within the code vector, i.e. ICI is replaced by ISI. Zero padding in the second block-spreading stage eliminates ICI and maintains the orthogonality among the MU and TH spreading stages.

[0061] Further, the multi-user separating front end of receiver 14, i.e. TH and MU de-spreading units 42 and 44 respectively, preserve the maximum-likelihood (ML) optimality. This is shown through equations (46-48) given below.

$$\check{\mathbf{G}} := (\mathbf{I}_M \otimes \check{\mathbf{C}}_{uA}) (\mathbf{I}_M \otimes \check{\mathbf{D}}_{uB}) \text{ where } \check{\mathbf{G}} := [\check{\mathbf{G}}_0, \check{\mathbf{G}}_1, \dots, \check{\mathbf{G}}_{N_f-1}] \quad (46)$$

$$\check{\mathbf{G}}^H \check{\mathbf{G}} = N_f \mathbf{I}_{M(K+L)} \quad (47)$$

$$\Pr[\mathbf{x}(i) | \{\mathbf{s}_u(i)\}_{u=0}^{N_u-1}] = \prod_{u=0}^{N_u-1} \Pr[\mathbf{y}_u(i) | \mathbf{s}_u(i)] \quad (48)$$

Equation (47) implies that if $\eta(i)$ is white, then $\hat{\eta}(i)$ remains white and the probability of receiving $\mathbf{x}(i)$, where $\mathbf{x}(i) := [\mathbf{x}_0^T(i), \mathbf{x}_1^T(i), \dots, \mathbf{x}_{M-1}^T(i)]^T$, given that $\mathbf{s}_u(i)$ is transmitted for each user equals the product of the probabilities of generating $\mathbf{y}_u(i)$ given that $\mathbf{s}_u(i)$ is transmitted. In other words, de-spreading matrices $\check{\mathbf{C}}_{uA}$ and $\check{\mathbf{D}}_{uB}$ reduce a multi-user detection problem, which operates on MUI and ISI in the presence of frequency selective channels, to an equivalent set of single user equalization problems without loss of ML optimality. As a result, if the N_u single user ISI problems in equation (44) are demodulated in the ML sense using for example, Viterbi's decoding, then computationally demanding multi-user detection is not needed when spreading and despreading matrices, \mathbf{D}_{uB} , \mathbf{C}_{uA} and $\check{\mathbf{C}}_{uA}$, $\check{\mathbf{D}}_{uB}$, are chosen according to equations (10, 15, 29, 30) respectively.

[0062] Single-user detector 46 removes channel effects from output blocks 45 and generates symbol block estimates given in equation (44). For example, linear equalizer Γ_μ outputs symbol block estimates in equation (44) according to equation (49).

$$\hat{\mathbf{s}}_\mu(i) = \Gamma_\mu \mathbf{y}_\mu(i) \text{ where } \hat{\mathbf{s}}_\mu(i) := [\hat{\mathbf{s}}_{\mu,0}^T(i), \hat{\mathbf{s}}_{\mu,1}^T(i), \dots, \hat{\mathbf{s}}_{\mu,M-1}^T(i)]^T \quad (49)$$

[0063] Single-user detector 46 performs symbol detection on soft estimates given by equation (49) and outputs the μ^{th} user's $K \times 1$ symbol block estimate 49 given by equation (50) according to decisions made by equation (51).

$$\check{\mathbf{I}}_\mu := [\check{\mathbf{I}}_\mu(iK), \check{\mathbf{I}}_\mu(iK+1), \dots, \check{\mathbf{I}}_\mu(iK+K-1)]^T \quad (50)$$

$$\check{\mathbf{I}}_\mu(iK+k) = \arg \max_m \{\hat{\mathbf{s}}_{\mu,m}(iK+k)\}, \text{ for all } m \in [0, M-1] \text{ and } k \in [0, K-1] \quad (51)$$

Trading off performance for complexity, any other detector may replace the maximum likelihood (ML) Viterbi equalizer, given in equation (44), typically used in multiple user detection. The complexity of ML sequence estimation (MLSE) for M-ary PPM modulation is $O(KM^{L_u})$ per symbol block of size K. Consequently, the per-symbol decoding complexity is $O(M^{L_u})$ regardless of the block size K and the maximum channel order. Depending on how Γ_μ is selected, the linear receivers including a matched filter (MF)

receiver, zero-forcing (ZF) receiver, and minimum mean-square error (MMSE) receiver are given in equations (52-54) respectively.

$$\Gamma_{\mu}^{MF} := \hat{\mathbf{H}}_{\mu}^H / N_f \quad (52)$$

$$\Gamma_{\mu}^{ZF} := (\hat{\mathbf{H}}_{\mu}^H \hat{\mathbf{H}}_{\mu})^{-1} \hat{\mathbf{H}}_{\mu}^H / N_f \quad (53)$$

$$\Gamma_{\mu}^{MMSE} := N_f \mathbf{R}_{\mu} \hat{\mathbf{H}}_{\mu}^H [\mathbf{R}_{\eta} + (N_f)^2 \hat{\mathbf{H}}_{\mu} \hat{\mathbf{H}}_{\mu}^H]^{-1} \quad (54)$$

Equation 54 can be expanded using substitutions according to equations (55, 56).

$$\mathbf{R}_{\mu} := E\{\mathbf{s}_{\mu}(i) \mathbf{s}_{\mu}^H(i)\} \quad (55)$$

$$\mathbf{R}_{\eta} := E\{\hat{\mathbf{q}}(i) \hat{\mathbf{q}}^H(i)\} \quad (56)$$

In this manner, the techniques allow transceivers to be configured with receivers in favor of low complexity or high performance.

[0064] FIG. 4 illustrates an example $N_c \times N_f$ matrix 50 for assigning the address $\{u_A, u_B\}$ to the u^{th} user with $N_c = 4$ and $N_f = 8$ of system 10. In order to facilitate separation of multiple users over a multi-path communication channel each user is assigned a TH address and a user-signature pattern that will herein be referred to as a (multi-user) MU address corresponding to a TH spreading code and an orthogonal spreading code respectively. Because communication system 10 uses an M-ary PPM-IRMA format, identical information bearing symbols are transmitted over N_f frames. Consequently, each symbol can be viewed as being encoded with a repetition code. Replacing the repetition code with an orthogonal spreading code specified by a MU address enables N_u users to be uniquely identified. The number of users that can be uniquely identified in an M-ary PPM-IRMA format communication system is given in equation (57).

$$N_u = N_c N_f \quad (57)$$

[0065] Matrix 50, therefore, illustrates TH and MU address assignments for a fully loaded system with 32 users. Each user is assigned a TH address vector of length $P_c = N_f$ according to equation (58).

$$\mathbf{c}_{uA} := [c_{uA}(0), \dots, c_{uA}(N_c - 1)]^T, \quad c_{uA}(n) \in [0, P_c - 1] \quad (58)$$

For any two users, u and μ , $\mathbf{c}_{uA}(n) \neq \mathbf{c}_{\mu A}(n)$, $\forall n \in [0, P_c - 1]$. The number of TH address vectors can at most be N_c , i.e. the number of chips in a frame. If the number of active users, N_u , is less than N_c then N_u of the N_c TH address vectors can be uniquely assigned to the N_u users. TH spreading codes are specified by a TH address, e.g. the u_A^{th} , and defined according to equation (59) with $c_{uA}(n)$ as defined in equation (6).

$$\mathbf{c}_{uA} := [c_{uA}(0), c_{uA}(1), \dots, c_{uA}(P_c - 1)]^T, \quad c_{uA}(n) \in [0, 1] \quad (59)$$

These TH spreading codes are mutually orthogonal, i.e. $\mathbf{c}_{uA}^H \mathbf{c}_{\mu A} = N_f \delta(u_A - \mu_A)$, $\forall u_A, \mu_A \in [0, N_c - 1]$. Replacing the repetition code with a set of spreading codes orthogonal to the set of orthogonal TH spreading codes allows a group of users with identical TH spreading codes to be uniquely identified. MU spreading codes given by a specific MU address, e.g. the u_B^{th} , can be expressed as given in equation (60).

$$\mathbf{d}_{uB} := [d_{uB}(0), \dots, d_{uB}(N_f - 1)]^T, \quad d_{uB}(n) \in [1, -1] \quad (60)$$

MU spreading codes are also designed to be mutually orthogonal. Equation (60) is designed such that $\mathbf{d}_{uB}^H \mathbf{d}_{\mu B} = N_f \delta(u_B - \mu_B)$, $\forall u_B, \mu_B \in [0, N_f - 1]$. The repetition encoding of a standard IRMA system can be achieved by setting $\mathbf{d}_{uB} = [1, 1, \dots, 1]$.

[0066] If the number of N_u users satisfies $N_u \leq N_c N_f$, then a given TH address is assigned to a group of $\lfloor N_u / N_c \rfloor$ or $\lceil N_u / N_c \rceil$ users. An additional set of MU addresses are assigned to be able to resolve users in the same group by employing a unique mapping to each of the $\lfloor N_u / N_c \rfloor$ or $\lceil N_u / N_c \rceil$ users in the same group. As a result, the same MU address can be assigned to several users that belong to different groups since groups are differentiated via their TH address. For example, the u^{th} user may be assigned TH address and MU address with index given by its $\{u_A, u_B\}$ pair according to equations (61, 62) respectively.

$$u_A = u \pmod{N_c} \quad (61)$$

$$u_B = u \pmod{N_f} \quad (62)$$

[0067] FIG. 5 is a block diagram that illustrates an example M-ary PPM-IRMA signal of the u^{th} user's information bearing symbols during the n^{th} chip duration. The u^{th} user's transmitted symbol during the n^{th} chip duration is $I_u(\lfloor n/N_c N_f \rfloor)$ 61. The orthogonal M-ary PPM of the u^{th} user in FIG. 1 can be viewed as having M parallel branches 62 with each parallel branch realizing a shifted version of the pulse stream. However, one branch out of the M parallel branches can be sufficiently selected depending on the symbol value.

[0068] The information stream, and thus $s_{u,m}(\lfloor n / (N_c N_f) \rfloor)$ 63, does not change over the duration of $N_f N_c$ chips ($N_f T_f$ seconds) and is spread by TH chip-rate sequence 5 as defined in equation (6) to generate chip sequence 64 in equation (7). From this chip-rate sampled model, the nonlinearly modulated PPM-IRMA format can be viewed as a linearly modulated multiple access format such as CDMA, TDMA, or FDMA. The chip-rate sampled signal 64 is pulse shaped and converted to an analog signal 68 by functions 66. The sum of the M branches of the nonlinearly modulated PPM-IRMA signal are summed together to form signal 69.

[0069] FIG. 6 illustrates the multi-user block-spreading stage with MU address matrices \mathbf{D}_{uB} as given in equation (10). Data stream 24 is generated from data stream 22 by multi-user block-spreading unit 23 within transmitter 12 of FIG. 3. Data stream 22 represents the data stream on the m^{th} branch of the u^{th} user with blocks of symbols of length K. Multi-user block-spreading unit 23 applies $Q \times K$ block-spreading matrix \mathbf{D}_{uB} to obtain data stream 24 given by the $Q \times 1$ output vectors in equation (9). Multi-user block-spreading unit 23 can be viewed as first symbol-spreading each of the K symbols into Q frame-rate blocks followed by frame-interleaving the Q frame-rate blocks.

[0070] FIG. 7 illustrates a sub-block of the TH block-spreading stage with TH address sub-matrix $\mathbf{C}_{uA}^{(0)}$ as given in equation (14). Data stream 26 is generated from data stream 24 by TH block-spreading unit 25 within transmitter 12 of FIG. 3. Data stream 26 represents the single block to be transmitted on the m^{th} branch of the u^{th} user and is given by equation (16). Each block of Q frames is spread into P chips with L zeros padded between blocks of P

chips. TH spreading unit 25 can be viewed as first symbol-spreading frame-rate blocks into P chips followed by chip-interleaving and zero padding.

[0071] FIG. 8 is a flowchart illustrating an example mode of operation of a transmitter 12 in communication system 10 in which MS-BS preserves the orthogonality of transmitted waveforms through communication channel 16 thereby deterministically eliminating MUI 18 at receiver 14. Generally, transmitter 12 parses an outbound serial data stream into blocks of K symbols (70) and subsequently applies $Q \times K$ MU block-spreading matrix \mathbf{D}_{uB} (71) corresponding to the specified user's assigned MU address. This spreads blocks of K symbols into Q frames (72) and interleaves the frames for the K symbols (73).

[0072] TH spreading matrix \mathbf{C}_{uA} of size $P \times Q$ is applied (74) according to the specified use's assigned TH address. TH spreading matrix \mathbf{C}_{uA} spreads blocks of Q frames into blocks of P chips (75) and interleaves the chips for the Q frames (76). Zeros are padded between each block of P chips (77). The chips that are generated from the same block of symbols are, therefore, temporally spaced and zero padded. In this manner, each block of K symbols produces $N_f N_c (K+L)$ interleaved chips, where L represents the number of zeros, N_f represents the number of frames over which an identical information bearing symbol is repeated, and N_c represents the number of chips per frame. Transmitter 12 converts the interleaved multi-stage block-spread chips into a serial bit stream (78) and generates a transmission waveform for carrying the data through communication channel 16 to receiver 14 (79).

[0073] FIG. 9 is a flowchart illustrating an example mode of operation of a receiver 14 in communication system 10. Receiver 14 serial to parallel converts the received chip-rate sampled sequence $P \times 1$ blocks (80). TH de-spreading matrix $\check{\mathbf{C}}_{uA}$ of size $P \times N_f (K+L)$ is applied to the $P \times 1$ blocks (81) reading subsets of $N_f (K+L)$ chips, each subset having P chips that were generated from the same symbol, thereby de-interleaving the chips (82). The de-interleaved chips are then de-spread into blocks of frames (83) to which MU de-spreading matrix $\check{\mathbf{D}}_{uB}$ of size $N_f (K+L) \times (K+L)$ is applied (84). $K+L$ subsets of the stored frames, each subset having $N_f K$ frames that were generated from the same block of symbols are de-interleaved (85) and de-spread into blocks symbols (86). Receiver 12 then applies a single user detector to eliminate channel effects and output symbols based on the soft estimates (87).

[0074] FIGS. 10 and 11 are graphs illustrating the BER performance of a matched filter (MF), RAKE, receiver of a conventional IRMA system against the MS-BS IMRA technique in the presence of frequency selective channels. A filter matched to the desired user's TH code and the number of users is confined to $N_u \leq N_c$ in the conventional IRMA system. For both systems, parameters are adjusted to avoid ISI and symbol by symbol reception is performed at the receiver.

[0075] FIG. 10 illustrates BER performance of the conventional IRMA system with one, two, three, and four users compared to an MS-BS IRMA system with the active number of users in the range of $1 \leq N_u \leq 32$ in an uplink scenario with perfect power control, i.e. P_u for all users. In particular, FIG. 9 illustrates how the BER performance of a conventional IRMA system degrades as the number of users increases while the BER performance of the MS-BS IRMA system remains invariant as the number of active users changes in the specified range.

[0076] FIG. 11 illustrates BER performance of the conventional IRMA system compared to an MS-BS IRMA system in an uplink scenario with two active users, user 1 being the desired user, and the effective transmission power of user 2 twice that of user 1. In particular, FIG. 10 illustrates that the imperfect power control has no effect on the BER performance of the MS-BS IRMA system while the conventional IRMA system encounters degradation in BER performance.

[0077] FIGS. 12 and 13 illustrate the BER performance over 1,000 channel realizations of the MS-BS IMRA technique compared to the MUD IRMA technique, which is applies only to the downlink scenario. FIG. 12 illustrates the MS-BS IMRA technique compared to the MUD IRMA technique with a zero-forcing (ZF) linear receiver. FIG. 13 illustrates the MS-BS IMRA technique compared to the MUD IRMA technique with a minimum mean-square error (MMSE) linear receiver.

[0078] Various embodiments of the invention have been described. The described techniques may provide advantages in multi-user communication systems. Conventional IRMA systems approximate MUI as Gaussian noise and use TH codes with strict power control to statistically suppress MUI. Applying a first and a second block-spreading code selected from a first and a second set of mutually orthogonal block-spreading codes in which the first and the second set of mutually orthogonal block-spreading codes are also orthogonal preserves the orthogonality of transmitted waveforms through frequency selective communications. As

a result, MUI and ISI can be eliminated deterministically. Such systems allow each user to use a different detector that can be chosen in favor for low complexity or high performance without interfering with other users.

[0079] “Multi-stage block-spreading” may use a first set of mutually orthogonal MU spreading codes, e.g. direct sequence CDMA codes or digital carrier frequency multiple access codes, and a second set of mutually orthogonal TH codes. The number of orthogonal mutually orthogonal MU spreading codes and TH spreading codes may be equal to the number of frames over which identical information bearing symbols, N_f , are transmitted and the number of chips per frame, N_c , respectively. As a result, a group of users may be assigned the same TH code with each user in the group uniquely identified by a different MU code. The maximum number of uniquely identified users in a MS-BS IRMA system is, therefore, $N_c N_f$, with each user identified by a MU address and TH address corresponding to a specific MU and TH code respectively.

[0080] Further, such systems do not exhibit BER degradation as the number of users increases and requires no additional power to achieve a specified BER regardless of the multi-path channel. Different users may also be provided with variable transmission rates by assigning a single user more than one set of MU and TH addresses.

[0081] The described techniques can be embodied in a variety of devices that communicate using ultra wideband communication, including base stations, mobile phones, laptop computers, handheld computing devices, personal digital assistants (PDA's), a device within a personal area network, a device within a sensor network, and the like. The devices may include a digital signal processor (DSP), field programmable gate array (FPGA), application specific integrated circuit (ASIC) or similar hardware, firmware and/or software for implementing the techniques. If implemented in software, a computer-readable medium may store computer readable instructions, i.e., program code, that can be executed by a processor or DSP to carry out one or more of the techniques described above. For example, the computer-readable medium may comprise random access memory (RAM), read-only memory (ROM), non-volatile random access memory (NVRAM), electrically erasable programmable read-only memory (EEPROM), flash memory, or the like. The computer-readable medium may comprise computer readable instructions that when executed in a wireless communication device, cause the wireless communication device to carry out one or

more of the techniques described herein. These and other embodiments are within the scope of the following claims.